

PATENT

1600.65221

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: John M. Garrett

Serial No.:

Filed: February 13, 2001

For: SEMICONDUCTOR JUNCTION
PROFILE AND METHOD FOR
THE PRODUCTION THEREOF

*#4
J. Francis
02/13/01*
I hereby certify that this paper is being deposited with
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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

jc997 U.S. PTO
09/782618
02/13/01


Dear Sir:

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant through counsel

herewith:

Submits copies of patents and publications as set forth in the attached form

PTO-1449 as follows.

UNITED STATES PATENTS

<u>Patent No.</u>	<u>Issued</u>	<u>Patentee(s)</u>
4,680,615	July 14, 1987	Götzenbrucker et al.

FOREIGN DOCUMENT

<u>Patent No.</u>	<u>Issued</u>	<u>Country</u>
396,326	Nov. 7, 1990	EPO
1,068,199	Nov. 23, 1964	Great Britain
2,468,207	April 30, 1981	France

PUBLICATIONS

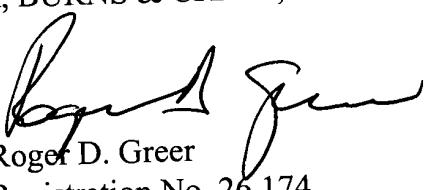
1. P.D. Taylor, "Thyristor Design and Realization", *Mechanical Bevelling*, 1987, pp. 41-45.

Applicant respectfully requests that the Examiner consider the above-listed references in the examination of this application and list these references of record in the application.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By


Roger D. Greer
Registration No. 26,174

February 13, 2001

300 South Wacker Drive
Suit 2500
Chicago, Illinois 60606
(312) 360-0080

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